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Complete if Known	
Application Number	10/075659
Filing Date	February 13, 2002
First Named Inventor	Mosley, Larry
Group Art Unit	2827
Examiner Name	Vigushin, John
Attorney Docket No: 884.209US2	

Sheet 1 of 1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date if Appropriate
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>PL</i>		"Capacitor for Multichip Modules", IBM Technical Disclosure Bulletin, Vol. 20, Issue 8, (01/01/1978), 3117-3118	

EXAMINER

John B. Vigushin

DATE CONSIDERED

July 24, 2003

Substitute Disclosure Statement Form (PTO-1449)
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